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FEB 25 2004
PATENT & TRADEMARK OFFICE

Docket No. 243479US0

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hidetoshi FUJIMOTO

SERIAL NUMBER: 10/677,224 ATTN: APPLICATION BRANCH

FILING DATE: October 3, 2003

FOR: SEMICONDUCTOR DEVICE HAVING A LATTICE-MISMATCHED SEMICONDUCTOR LAYER ON A SUBSTRATE

## FILING OF DECLARATION UNDER 37 CFR 1.53(f)

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

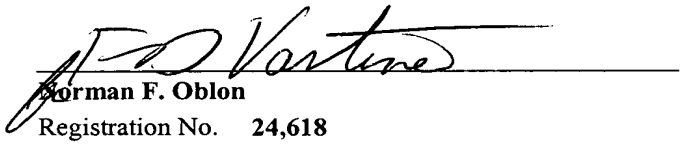
Responsive to the notification dated January 8, 2004, and in accordance with the provisions of 37 CFR 1.53(f), Applicants submit herewith a Rule 63 Declaration.

The required fee was paid at the time of filing the application.

In light of the foregoing, this application is deemed to be in proper condition for examination and such favorable action is earnestly solicited.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



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